

İBRAHİM AYDIN COŞKUNER

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Career Summary

A Digital Design Engineer with 5 years experience on Digital Hardware Design and Embedded Software Development. Xilinx Design Flow and VHDL has been used for digital circuit designs simulations and verifications on Xilinx FPGAs. Embedded Software development is done on a DSP for various projects using C and Assembly languages. Able to work well in teams, reliable and organized, and enthusiastic to learn.

EDUCATION

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| 2003-2006 | Middle East Technical University, Ankara
M.S., Electrical and Electronics Engineering
Thesis Title: "A Novel Fault Tolerant Architecture on a Runtime Reconfigurable FPGA"
GPA: 3.79 / 4 |
| 1999-2003 | Middle East Technical University, Ankara
B.S., Electrical and Electronics Engineering
GPA: 3.19 / 4
Favorite Courses: Analog Electronics, Digital Electronics, Introduction to Logic Design, Signals and Systems, C Programming, Data Structures in C++, Computer Networks
Projects: <ul style="list-style-type: none">▪ A MOSFET tester design implemented with the HP VEE Software▪ A 2x5-bit Array Multiplier is designed and implemented with Xilinx Software and Hardware▪ An automatic Traffic System Design with an intelligent vehicle that can be directed to a destination by the help of a computer. |
| 1992-1999 | Selçuklu Anatolian High School, Konya
High School Diploma |

WORK EXPERIENCE

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| April 2008-Current | A World Leader EDA Software Company, Ankara
Corporate Applications Engineer <ul style="list-style-type: none">▪ Give support to the Electronic System Level (ESL) design product (used for creating hardware IP from MATLAB/Simulink)▪ Define and develop tests for new features of the product▪ Create specifications for new features of the product▪ Develop IP designs using the tool library of the product▪ Give pre-sales and post-sales technical support to customers and sales team |
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- June 2003-March 2007 A Defense Industry Institute, Ankara
 Digital Design Engineer
- Design of a SpaceWire Node that is compliant with ECSS-E-50-12A. The design is done with VHDL, implemented and verified on a Xilinx FPGA
 - An interface design is implemented on the FPGA that connects a Processor to a Compact Flash Card.
 - A USB (Universal Serial Bus) interface design with a TI DSP processor, Xilinx FPGA and Philips PDIUSB12 IC (Embedded software, hardware and PC Software design is done).
 - A NAND Flash Memory interface design with TI DSP processor and Xilinx FPGA.
 - Designing and testing the functionality of Electronic Cards that includes digital hardware
- July-August 2002 ASELSAN A.Ş. MGEO Division, Ankara
 Summer Trainee
- An UART (Universal Asynchronous Receiver-Transmitter) design with VHDL on FPGA
- June-July 2001 TÜBİTAK BİLTEN (UZAY), Power Electronics Group, Ankara
 Summer Trainee
- 220V AC -12V DC Power Supply Design

OTHER COURSES AND SEMINARS

- Signal Processing with MATLAB by FİGES A.Ş., Ankara, Dec 2005
- Signal Processing with MATLAB (2 days)
 - Signal Processing with SIMULINK (1 day)
- DSP and Embedded Applications for Xilinx Virtex-4 by SoLogic, Ankara, June 2006
- Designing with the Virtex-4 Family (2 days)
 - DSP Implementation Techniques for Xilinx FPGAs (3 days)
 - Embedded Systems Development (2 days)
 - DSP Design Flow (3 days)
 - Advanced Features and Techniques of Embedded Systems Development (2 days)
 - MicroBlaze System Implementation (1 day)

SKILLS and ABILITIES

Computer

Programming Languages: VHDL, C, C++, Java, Assembly, Perl, Html
 Operating Systems: Windows
 Application Software: Borland C++ Builder, Visual C++, MS Office, MS Visio
 Programs for Electronics: HP VEE, MATLAB/Simulink, Xilinx ISE, Xilinx ChipScope Pro, Mentor Modelsim, Electronics Workbench, Pspice, , Protel 99, Protel DXP, TI Code Composer Studio, Altera Quartus, Synplify Pro, Synplify DSP

Languages

Turkish (Native Language), Fluent English, Intermediate German

ADDITIONAL INFORMATION

Military Service: Completed at April 2007 - March 2008
Driving License: B type
Hobbies: Paragliding, Web Page design, Football

References available upon request